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This note is focused on analog electronics with an emphasis on transistor level design of integrated circuits. Topics to be covered includes transistor amplifiers, integrated circuit biasing techniques, output stage design and IC amplifier building blocks, frequency response of amplifiers, stability and compensation techniques for amplifiers using negative feedback. Author(s): David A. Johns ...

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*IEEE Custom Integrated Circuits
Conference Best Paper Award B.
Razavi, "CMOS Technology
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RF Design," (Invited) Proc. of
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